WHAT IS CLAIMED IS:

1   1.   A magnetic card reader adapted to make a magnetic card and

2   at least one magnetic head to relatively move with respect to each

3   other and also adapted to demodulate data, which is recorded on

4   said magnetic card and obtained by said magnetic head, said magnetic

5   card reader comprising:

6   two magnetic heads arranged in a direction, in which each of

7   said magnetic heads relatively moves with respect to said magnetic

8   card, and adapted to take same data from said magnetic card and

9   to obtain two demodulated data;

10   an error detecting portion for detecting an error in at least

11   one of the two demodulated data; and

12   an error correcting portion for correcting the error, which

13   is detected by said error detecting portion, by using the other

14   demodulated data.

1   2.   The magnetic card reader according to claim 1, wherein said

2   error correcting portion is adapted to correct errors, which occur

3   in the demodulated data, character by character.

1   3.   The magnetic card reader according to claim 2, wherein said

2   error detecting portion is adapted to detect whether or not a parity

3   of the modulated data corresponding to each character is correct.

1   4.   A magnetic data demodulating method of making a magnetic card

2   and at least one magnetic head to relatively move with respect to

3   each other and demodulating data, which is recorded on said magnetic

4   card and obtained by said magnetic head, said method comprising

5   the steps of:

6   providing two magnetic heads in such a manner as to be arranged

7   in a direction, in which each of said magnetic heads relatively

8   moves with respect to said magnetic card, and taking same data from

9   said magnetic card to thereby generate two demodulated data;

10   detecting an error in at least one of said two demodulated

11   data; and

12   correcting the detected error by using the other demodulated

13   data.

1   5.   The magnetic data demodulating method according to claim 4,

2   wherein after the two modulated data are stored in a memory as binary

3   data represented by bits each having a binary value of "1" or "0",

4   the modulated data, which is an aggregate of the binary data, is

5   corrected character by character.